

FIG. 1

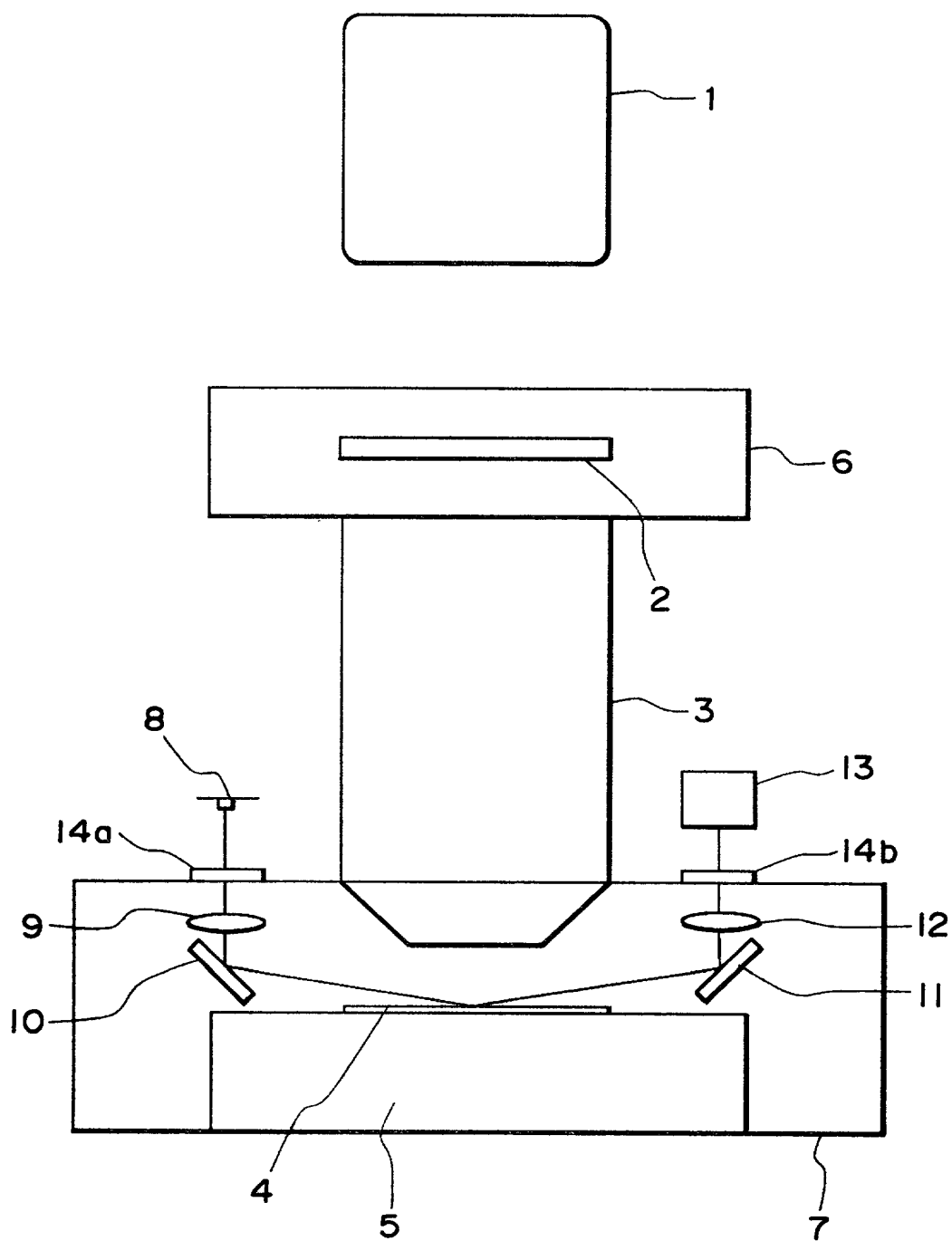


FIG. 2

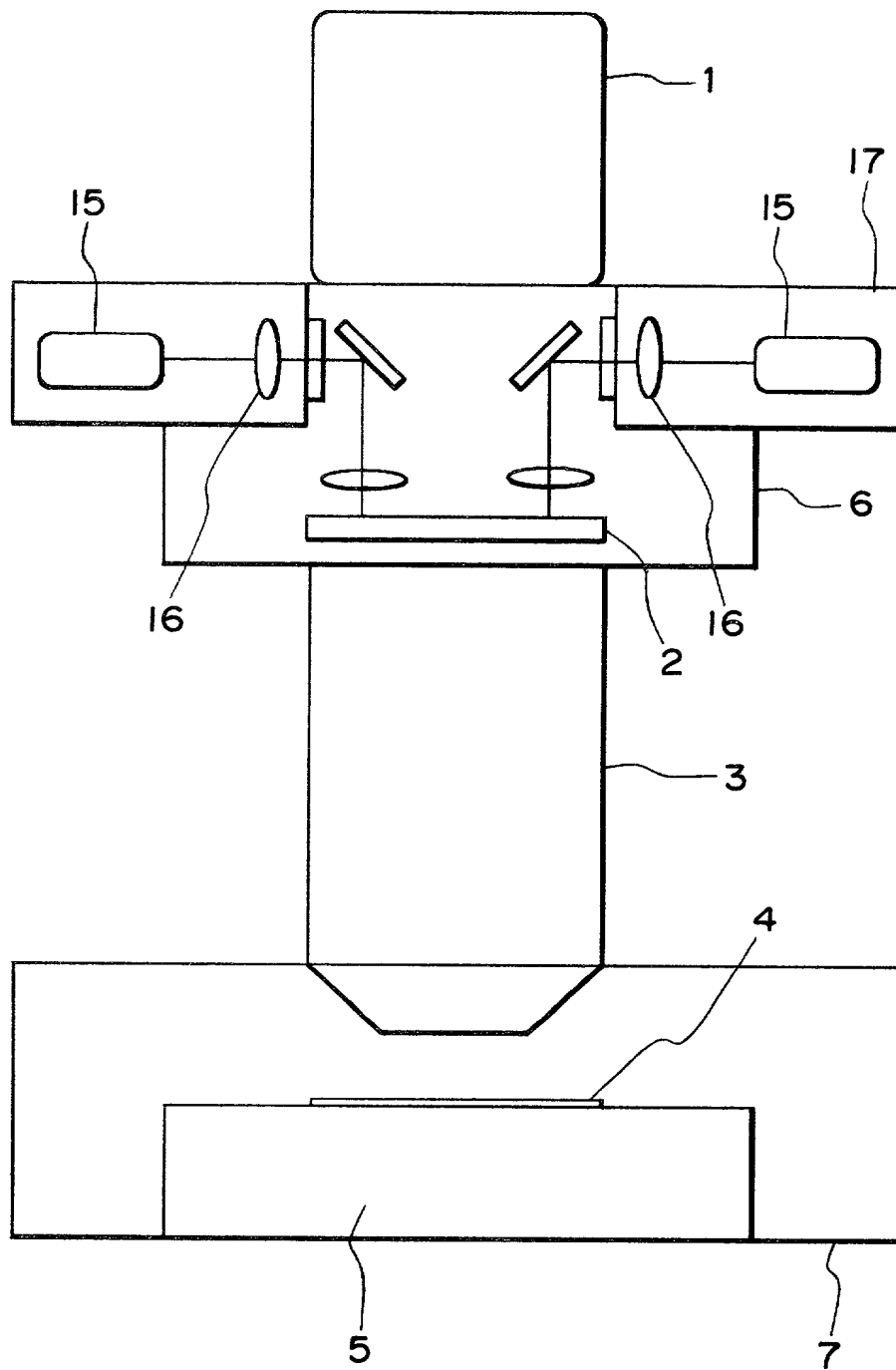


FIG. 3

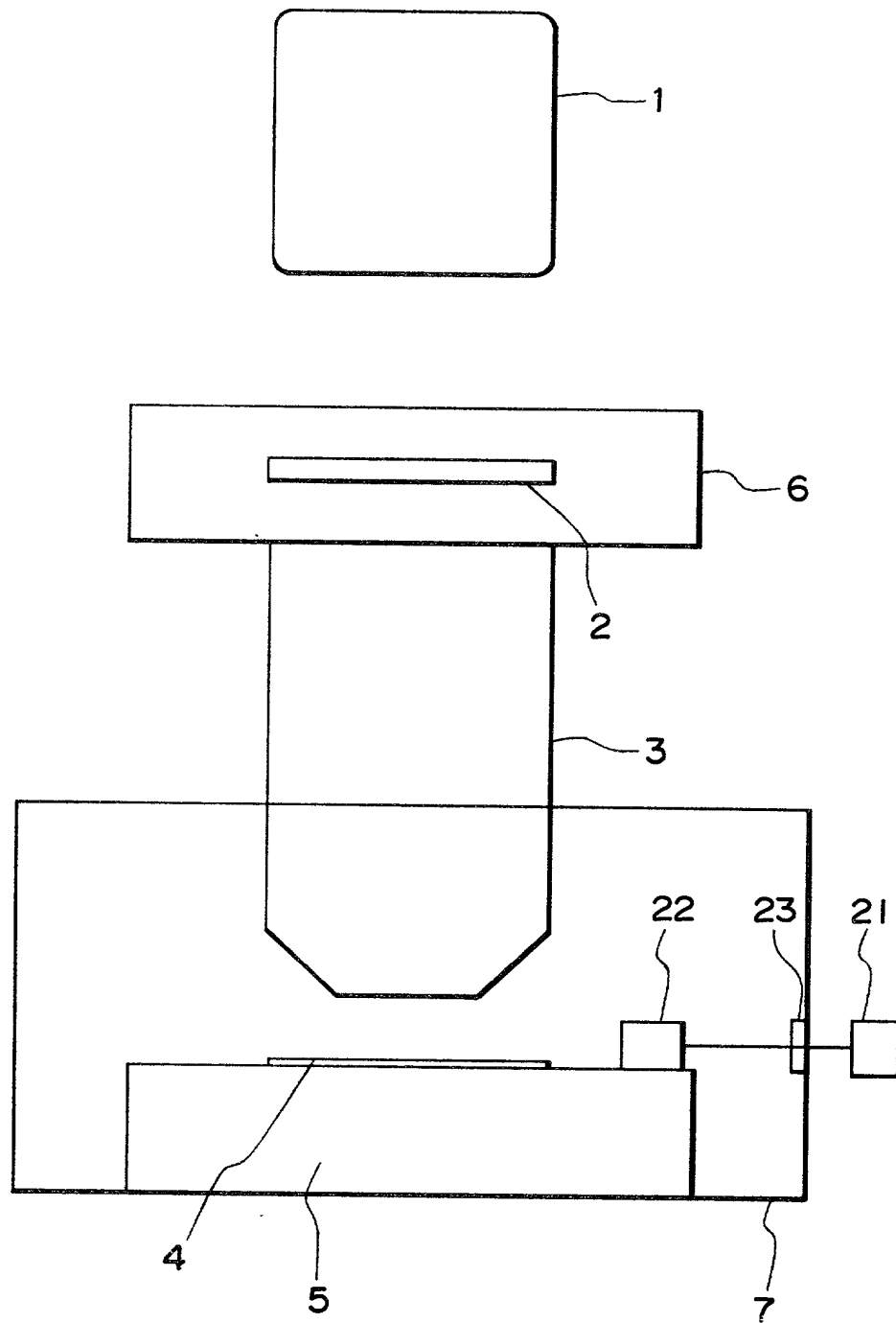


FIG. 4

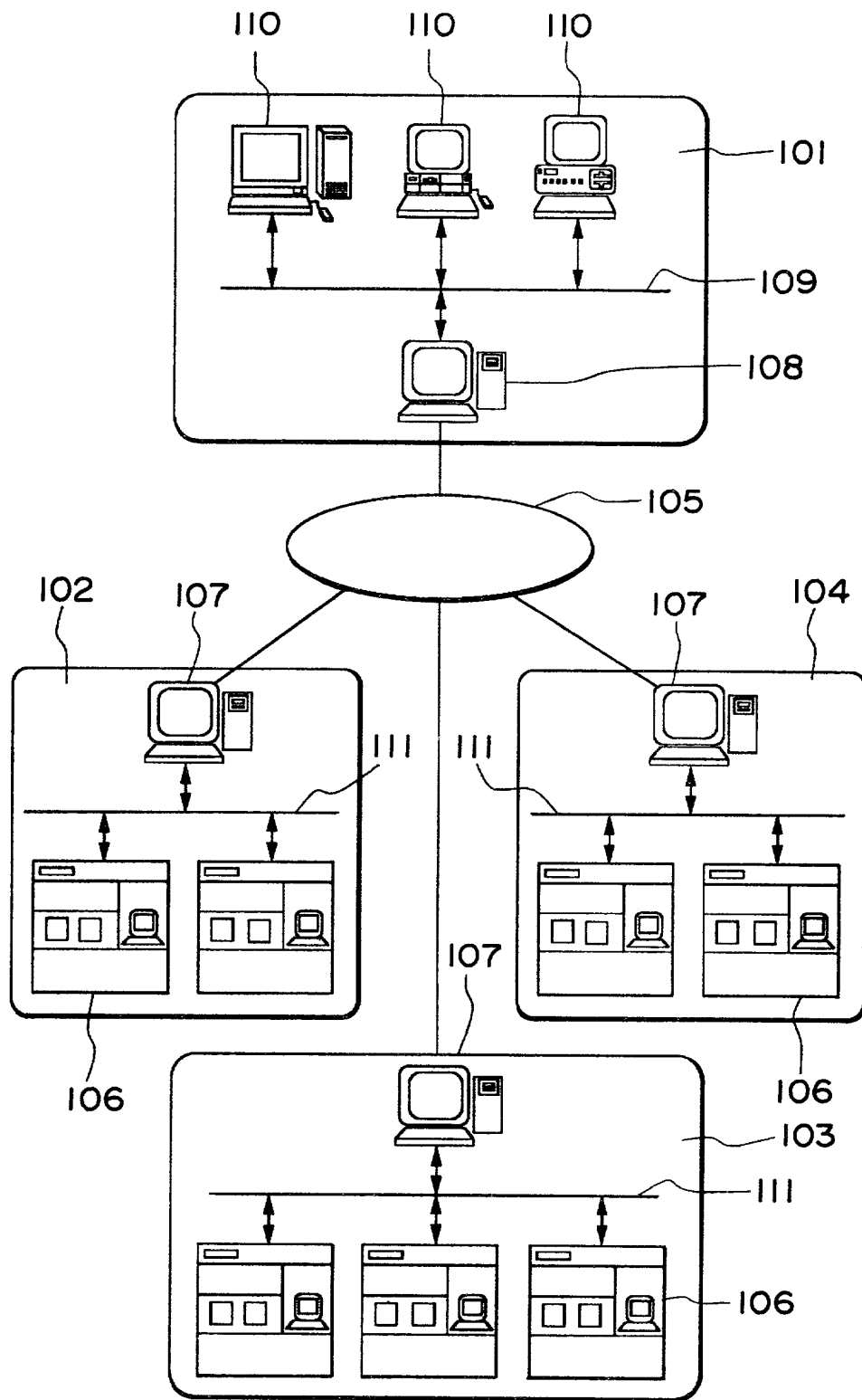


FIG. 5

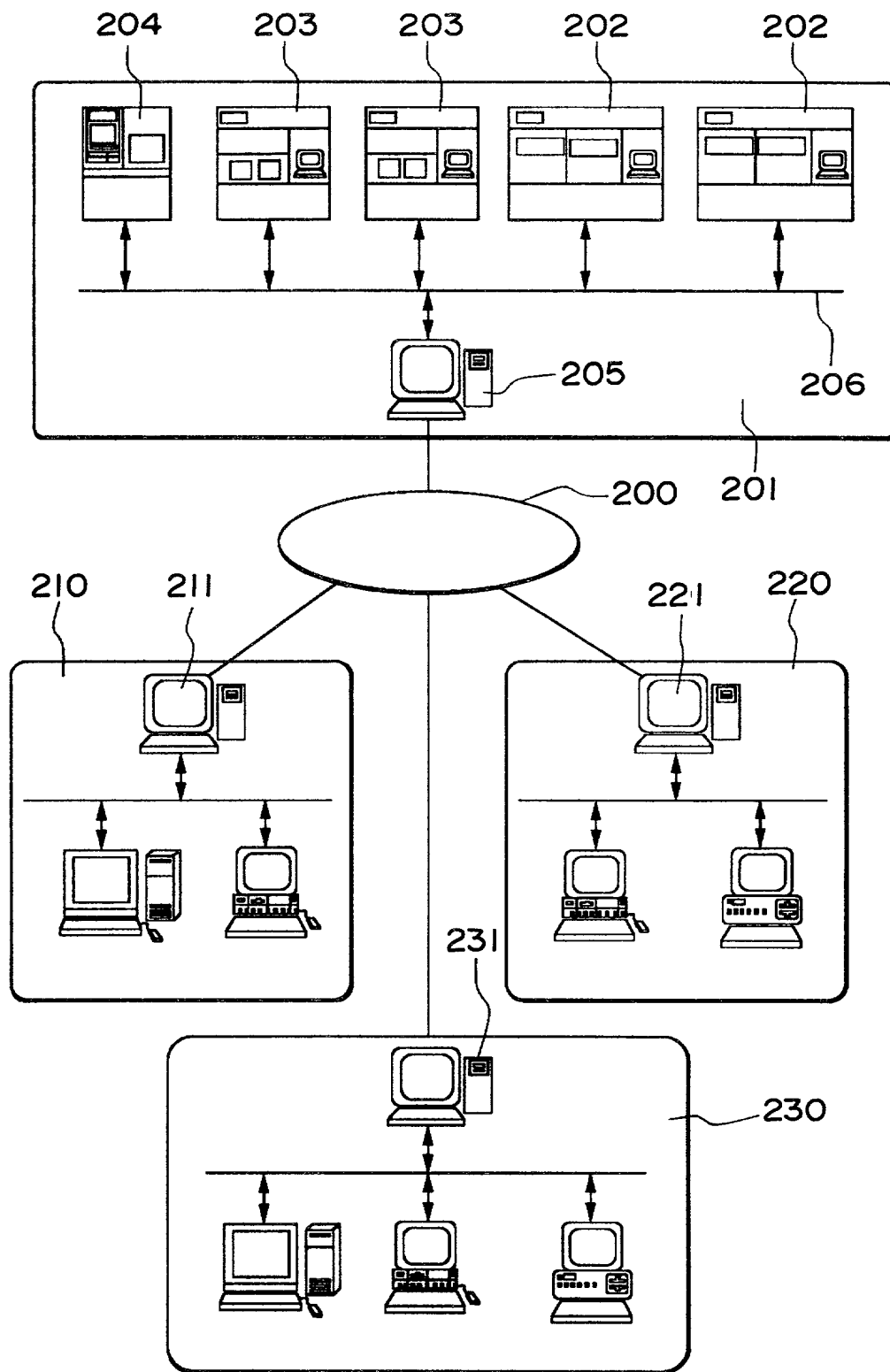


FIG. 6

URL

TROUBLE DB INPUT WINDOW

DATE

404

MACHINE

401

FILE

403

S. N. S/N

402

EMERGENCY

405

STATE

LED FLICKERING AFTER POWERED

↑

↓

↶

↷

406

SOLUTION

RESTART:PRESS RED BUTTON

↑

↓

↶

↷

407

PROGRESS

TEMPORARY SOLUTION DONE

↑

↓

↶

↷

408

NEXT

RESET

410

411

412

LINK TO RESULT
LIST DATABASE

SOFTWARE
LIBRARY

OPERATION
GUIDE

FIG. 7

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graph TD
    A[CIRCUIT DESIGN] -- "(STEP 1)" --> B[MASK MANUFACTURE]
    B -- "(STEP 2)" --> C[WAFER PROCESS  
(PRE-PROCESS)]
    D[WAFER MANUFACTURE] -- "(STEP 3)" --> C
    C -- "(STEP 4)" --> E[ASSEMBLY PROCESS  
(POST-PROCESS)]
    E -- "(STEP 5)" --> F[INSPECTION]
    F -- "(STEP 6)" --> G[SHIPMENT]
    G -- "(STEP 7)" --> H[ ]
  
```

FIG. 8

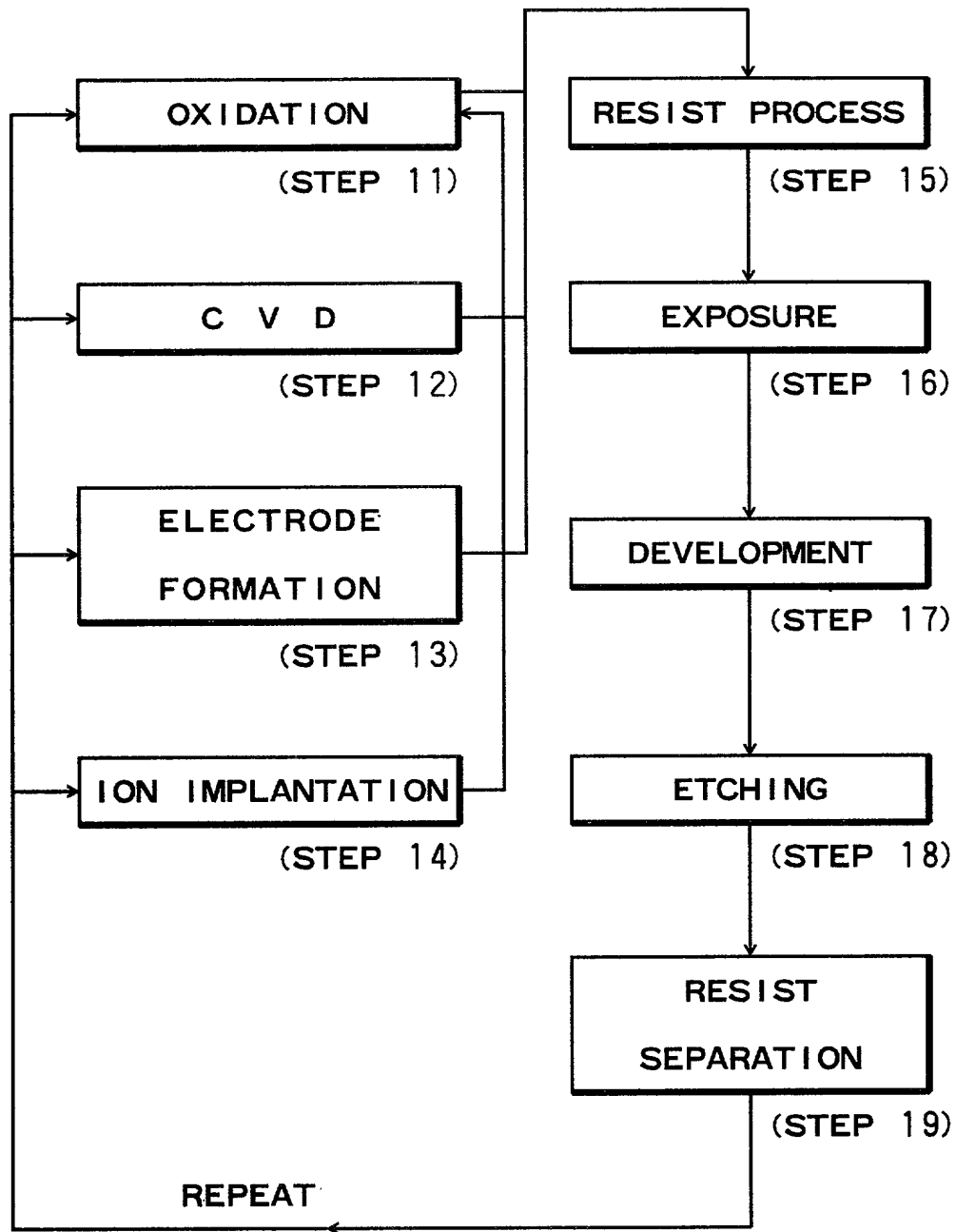


FIG. 9